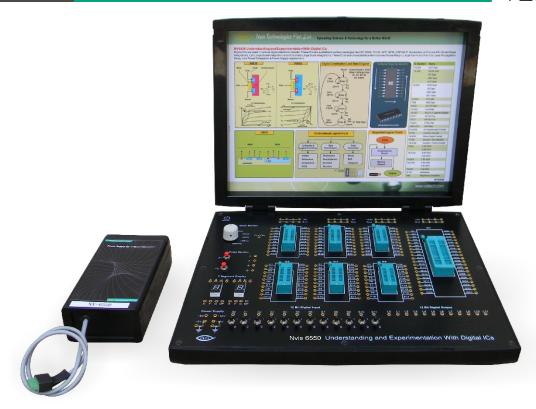


## Understanding and Experimentation with Digital ICs Nvis 6550

TECHBOOK



**Nvis 6550 Understanding and Experimentation with Digital ICs** is a training product which provides complete flexibility for hands on learning of a wide range of experiments in digital electronics. This product provides vast learning scope for students to design their own experiments and applications. Students can use digital IC's and connect their Input and Output to design and implement in the circuit. Nvis 6550 can be a part of library and can be issued to students to perform the experiments.

## **Features**

- Illustration of Combinational and Sequential circuits
- ZIF Socket provided for easy connections
- Compact size
- Simultaneous use of multiple ICs
- Online product tutorial

## **Scope of Learning**

- Study of Basic Gates
- Study of Half Adder using Logic Gates
- Study of Full Adder
- Study of Half and Full Subtractor using Logic Gates
- Study of Magnitude Comparator
- Study of Encoder / Priority Encoder
- Study of Decoder & Demultiplexers
- Study of Multiplexers
- Study of BCD to 7 Segment Display
- Study of Flip-Flop
- Study of Register
- Study of Counter

## **Technical Specifications**

**Mains Supply** : 90V - 250V, 50Hz

**Fixed DC Power Supply** : +12V, -12V, +5V, -5V

**Fixed TTL Generator** : 1Hz, 10Hz, 100Hz, 1kHz, 10kHz

and 100kHz

Pulse Generator : 5V

**ZIF Socket** : 20 Pins (6 Nos.)

40 Pins (1 No.)

**Input Section** : 12 toggle switches

Output Section : 12 LED indicator

Display : 7 Segment (2 Nos.)

**Dimension (mm)** : W 350 x D 280 x H 55