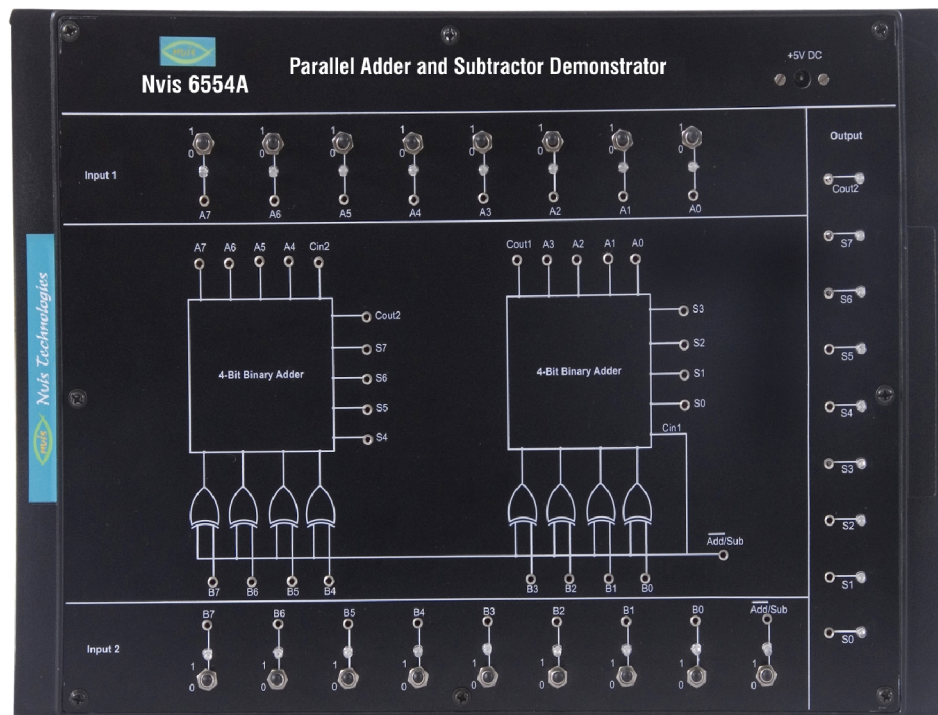




# Parallel Adder and Subtractor Demonstrator

## Nvis 6554A



**Nvis 6554A Parallel Adder and Subtractor Demonstrator** is a ready to use digital electronics experiment trainer. This product has been designed specifically for the students to understand the concept of parallel binary addition and subtraction methods for 4 bit and 8 bit binary numbers.

A Parallel Adder is a combinational logic circuit which has various registers to input the data and thus generate the output. Because of this, input bits are applied simultaneously and the sum appears at the output almost immediately. The parallel subtractor circuit works in similar manner where the difference appears as soon as the inputs are applied.

This product has logic switches with LEDs for logic high and low input facility. Besides this, a +5V adaptor is provided for DC power supply.

### Features

- 4-bit/8-bit Binary Addition and Subtraction
- Easy switching between addition and subtraction modes
- LEDs for visual indication of input-output and carry out logics
- Online product tutorial

### Technical Specifications

DC Power Supply	: +5V DC
Logic levels	
+5V	: High (logic 1)
0V	: Low (logic 0)
LED indication	: LED will be ON for logic high or '1' state and will be OFF for Logic low or '0' state
Dimensions (mm)	: 345 W x 240 D x 110 H

Designed & Manufactured in India by

**Nvis Technologies Pvt. Ltd.**

141-A, Electronic Complex, Pardesipura, Indore-452010, India.

© +91-731-4211500, info@nvistech.com, www.NvisTech.com