Nvis 6551 Experimentation with Logic Gates is an elite training system for the digital laboratories. It is useful in understanding the basic concepts and functioning of logic gates and their truth tables.

Nvis 6551 describes the AND, OR, NOT, NAND, NOR, XOR and XNOR gates in a very easy way. The training system is designed in such a way that for performing any experiment students have to connect the links by patch cords, so it is very helpful for students to learn the inputs and outputs of different logic gates.

Attractive input and output sections are provided on trainer in such a manner so that multiple experiments can be performed simultaneously.

Features

- **Exclusive and compact design**
- **Straight forward representation of all logic gates**
- **+5V SMPS Adaptor provided with the trainer for power supply**
- **Designed by considering all the safety standards**
- **Online product tutorial**
- **Low cost product including illustration of all logic gates**

Scope of Learning

- Study of AND gate and to verify its truth table
- Study of OR gate and to verify its truth table
- Study of NOT gate and to verify its truth table
- Study of NAND gate and to verify its truth table
- Study of NOR gate and to verify its truth table
- Study of XOR gate and to verify its truth table
- Study of XNOR gate and to verify its truth table

Technical Specifications

Input	: +5V DC
Logic levels	
+5V	: HIGH (Logic 1)
0V	: LOW (Logic 0)
Dimensions (mm)	: W 240 x D 345 x H 110
Weight	: 1 Kg.