



Nvis 6551A Experimentation with Characteristics of TTL and CMOS is a compact product designed to explain the fundamentals of Transistor–Transistor Logic (i.e. TTL) and Complementary Metal Oxide Semiconductor (i.e. CMOS), like Voltage Transfer Characteristics, Noise Margin and Gate Delay. Inbuilt Function Generator and DC supply is provided for ease of operation.

The Voltage Transfer Characteristic of a logic gate is a graph between gate output voltage and gate input voltage. Noise margin is the voltage difference by which the signal exceeds the threshold voltage for Logic Low or Logic High. Gate delay is the span of time starting from the instant when the input to a logic gate becomes stable, to the time that the output of that logic gate becomes stable.

Features

- In-built variable DC power supply
- In-built Function generator
- Illustration of both TTL and CMOS ICs
- Online product tutorial

Technical Specifications

DC power supply	: +12V, +5V
Variable DC power supply:	0 to +12V 0 to +5V
Pulse Generator	
Frequency	: 100kHz ±15%
Amplitude	: 5V
Mains Power supply	: 230V AC ±10%, 50Hz
Dimensions (mm)	: W 345 x L 240 x D 110

Scope of Learning

- To study the voltage transfer characteristics, noise margin and gate propagation delay of TTL NAND gate
- To study the voltage transfer characteristics and gate propagation delay of TTL Schmitt trigger NAND gate
- To study the voltage transfer characteristics, noise margin and gate propagation delay of TTL NOT gate
- To study the voltage transfer characteristics and gate propagation delay of TTL Schmitt Trigger NOT gate
- To study the voltage transfer characteristics, noise margin and gate propagation delay of CMOS NAND gate
- To study the voltage transfer characteristics and gate propagation delay of CMOS Schmitt trigger NAND gate
- To study the voltage transfer characteristics, noise margin and gate propagation delay of CMOS NOT gate
- To study the voltage transfer characteristics and gate propagation delay of CMOS Schmitt Trigger NOT gate